

Practical Example of Use

Testing Directional Overcurrent Protection

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Preface

This paper describes how to test directional overcurrent protection elements. It contains an application example that will be used throughout the paper. The theoretical background of the directional overcurrent protection will be explained. This paper also covers the definition of the necessary **Test Object** settings as well as the **Hardware Configuration** for directional overcurrent tests. Finally the *Overcurrent* test module is used to perform the tests that are needed for the directional overcurrent protection function.

Supplements: Sample *Control Center* file **Example_Overcurrent_OvercurrentDirectional.occ** (referred to in this document).

Requirements: *Test Universe* 3.00 or later; *Overcurrent* and *Control Center* licenses.

1 Application Example

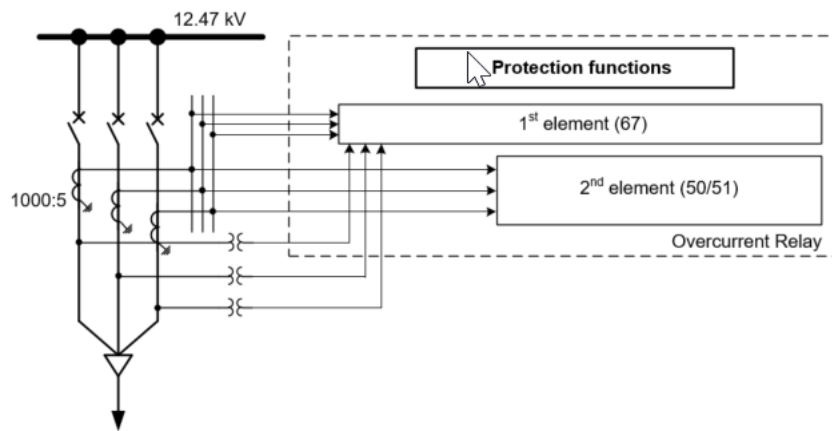


Figure 1: Feeder connection diagram for the application example

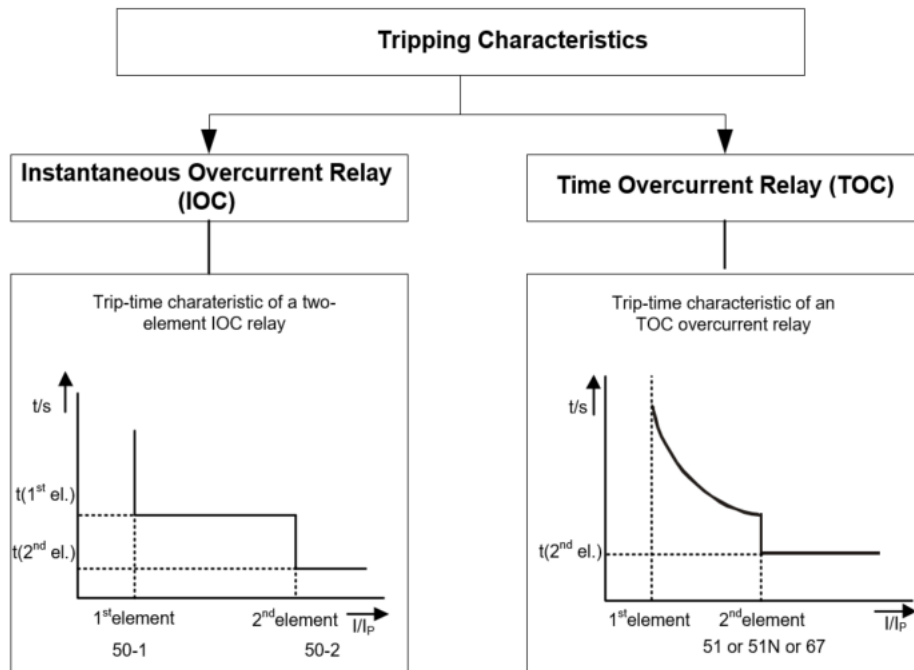
Parameter Name	Parameter Value	Notes
Frequency	60 Hz	
VT (primary/secondary)	7200 V / 69.3 V (L-N)	
CT (primary/secondary)	1000 A / 5 A	
1 st element	IEEE Very Inverse	Tripping characteristic
	Directional Fwd	Directional characteristic Forward
	1.5 X CT	Pick-up 1.5 x I_n CT primary
	3	Time multiplier setting (TD; TMS; τ_P , etc.)
	30°	Relay characteristic angle (directional protection)
2 nd element	0.7 X VT	Polarizing Voltage Threshold (directional protection)
	Definite Time	Tripping characteristic
	3 X CT	Pick-up 3 x I_n CT primary
	100 ms	Trip time delay

Table 1: Relay parameters for this example

2 Theoretical Introduction to Overcurrent Characteristics

2.1 Theory

There are two major overcurrent characteristic types: Definite-time (IOC) and Inverse-time (TOC)



The definite-time (IOC) characteristic allows for rapid fault clearance for current above a specific setting. In North America, inverse-time (TOC) curves conforming to the IEEE C37.112-1996 standard are common. The IEEE curves are derived from the formulae:

$$T = \text{Time Multiplier} * \left[\frac{A}{\left(\frac{I}{I_{pickup}}\right)^P - 1} + B \right], T_{RESET} = \text{Time Multiplier} * \left[\frac{t_r}{1 - \left(\frac{I}{I_{pickup}}\right)^2} \right]$$

IEEE Curve Shape	A	B	P	t_r
IEEE Extremely Inverse	28.2	0.1217	2.000	29.1
IEEE Very Inverse	19.61	0.491	2.000	21.6
IEEE Moderately Inverse	0.0515	0.1140	0.02000	4.85

Table 2: Inverse TOC tripping characteristics (GE850 Manual and IEEE C37.112-1996, section 4.2-4.4)

Where:

T = operate time (in seconds)

Time Multiplier = Multiplier setting

I = input current

I_{pickup} = Pickup Current setting

A, B, p = constants

T_{RESET} = reset time in seconds (assuming energy capacity is 100% and RESET is "Timed")

t_r = characteristic constant

2.2 Tripping Characteristics (50, 51, 67)

As the properties of the operational equipment differ considerably (overload, short circuit behavior, etc.) the characteristics have to be adapted to this.

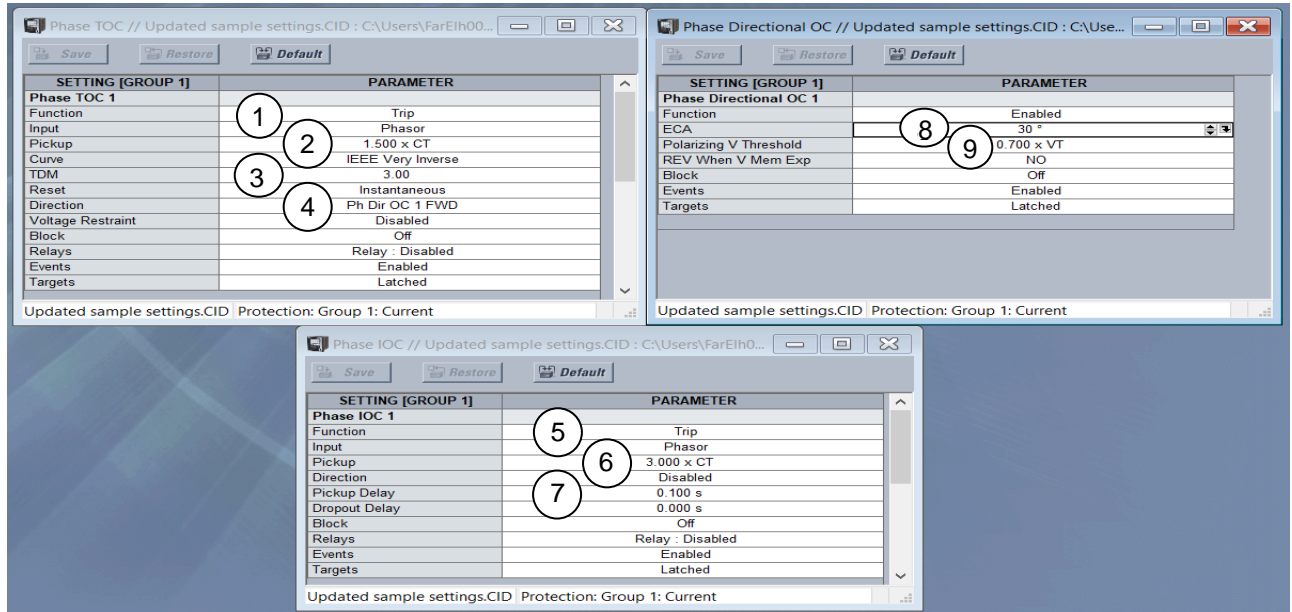


Figure 2: Parameters of an overcurrent relay (GE850)

1. Tripping characteristic for the 1st element (for this example Phase TOC1 - IEEE Very Inverse)
2. Pick-up setting of 1st element (CT=1000A)
3. Time multiplier setting (TDM) for the 1st element
4. Directional function (for this example forward)
5. Tripping characteristic for the 2nd element (Phase IOC1 for this example)
6. Pick-up setting (primary) of 2nd element
7. Trip time delay of 2nd element
8. Element characteristic angle ECA (only for the directional function)
9. Polarizing voltage threshold (VT=7200V L-N)

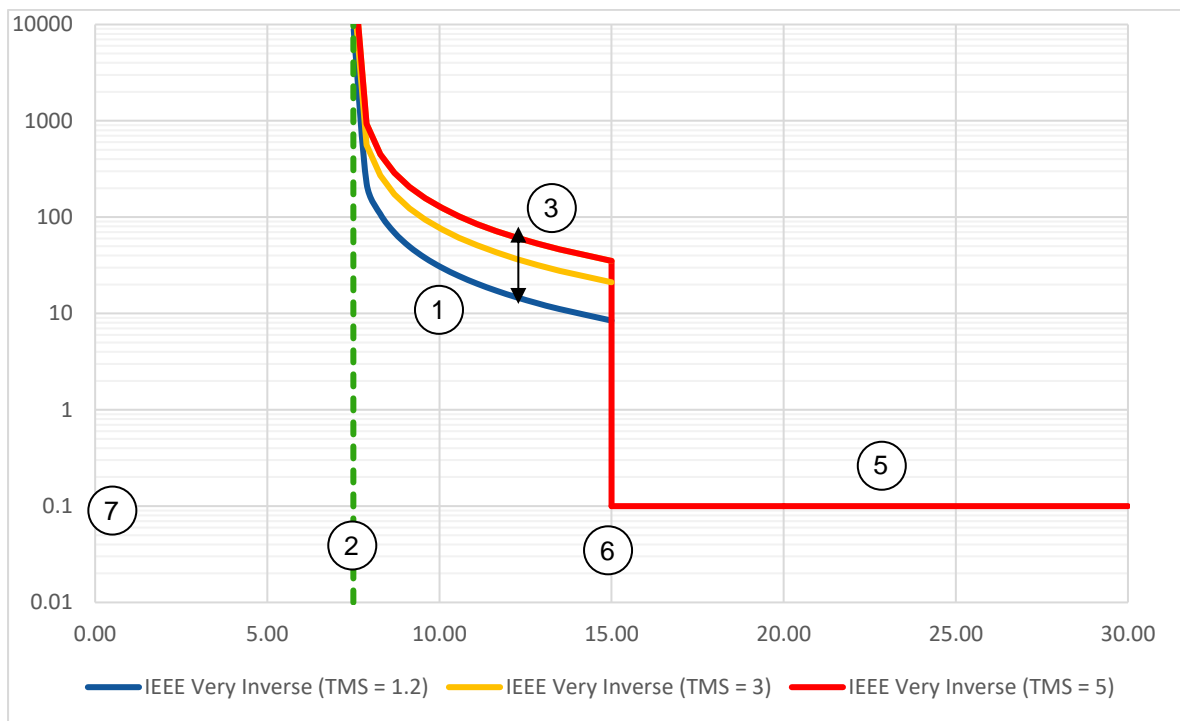


Figure 3: Comparison of IEEE Very Inverse tripping characteristics with different time multiplier settings (TDM)

2.3 Directional Overcurrent Protection (67)

Directional overcurrent protection is used in situations where fault current can flow in both directions through the relay location.

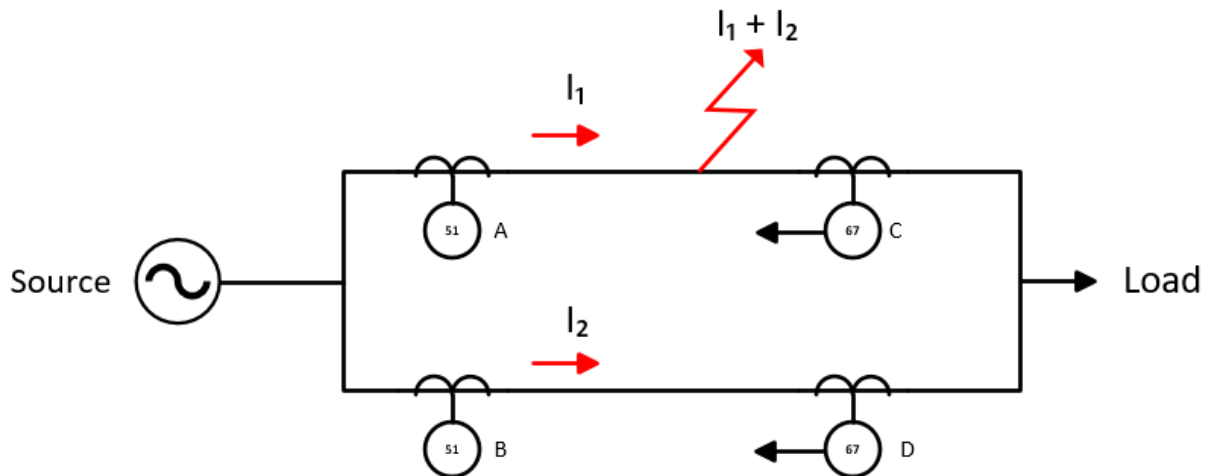


Figure 4: Fault current flow on a parallel feeder

The relays at C and D in the figure above are set to operate for faults in the forward direction (looking towards B for relay D and looking towards A for relay C). Since the flow of fault current is in the reverse direction at relay D, it does not operate. The fault on AC will be isolated by relay C.

In order to determine the current direction, forward or reverse, the relay needs a reference or polarizing voltage. The polarizing voltage is one whose phase angle will remain reasonably constant between a non-faulted and faulted system.

The following conditions should be considered when selecting the polarizing voltage:

- > For a close-in fault, the fault voltage is almost zero
- > The angle of the directional characteristic (ECA or MTA) depends on the fault type (Phase A-Ground, Phase B-Ground, etc.)

Note: The ECA (element characteristic angle) or MTA (maximum torque angle) is the angle by which the polarizing voltage is shifted in order to produce maximum relay sensitivity.

For this, relay connections have been developed that make use of different reference voltages with corrected phase angles.

Connections	Advantages	Disadvantages	Maximum Torque Occurs When
$0^\circ (\bar{I}_{ph}, \bar{V}_{ph})$	Maximum sensitivity with arc faults	Not suitable for HV-systems, no directional decision with a close-in fault	I lags 0°
$30^\circ (\bar{I}_{ph}, \bar{V}_{ph} - \bar{V}_{ph} \cdot \bar{a})$ $60^\circ (\bar{I}_{ph}, -\bar{V}_{ph} \cdot \bar{a})$		Reference voltage depends on the fault	I lags 30° I lags 60°
$90^\circ (\bar{I}_{ph}, \bar{V}_{ph} \cdot \bar{a}^2 - \bar{V}_{ph} \cdot \bar{a})$	Maximum reference voltage with phase to ground and phase to phase faults	Not suitable for arc faults	I lags 45° or 60°

Table 3: Relay connection for determining the reference voltage.

Phasor rotation operators:

$$\bar{a} = 1 \angle 120^\circ \quad \bar{a}^2 = 1 \angle -120^\circ$$

Note: The method used for the reference voltage depends on the relay manufacturer. For the following discussion we use an overcurrent relay with 90° relay connection and a relay characteristic angle (ECA or MTA) of 30° (I lags 60°).

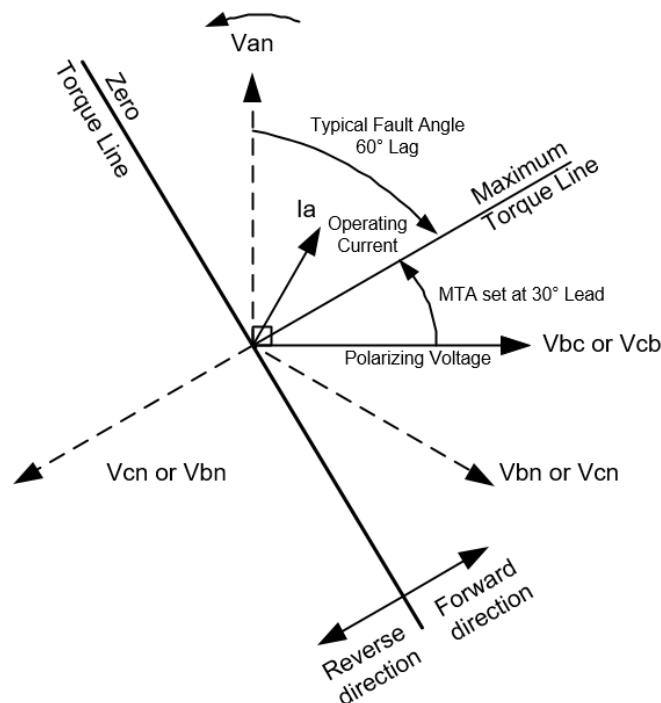
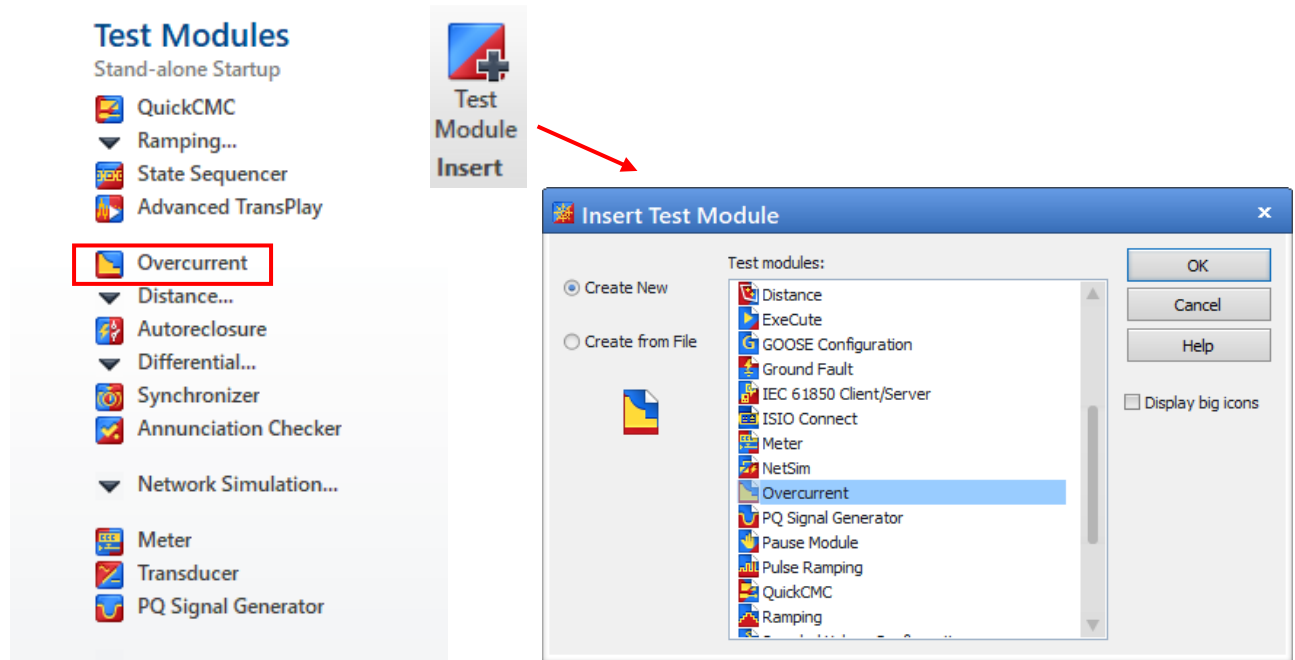


Figure 5: 90° relay connection with a relay characteristic angle of 30° (Phase A-Ground fault)

3 Practical Introduction to Overcurrent Characteristic Testing

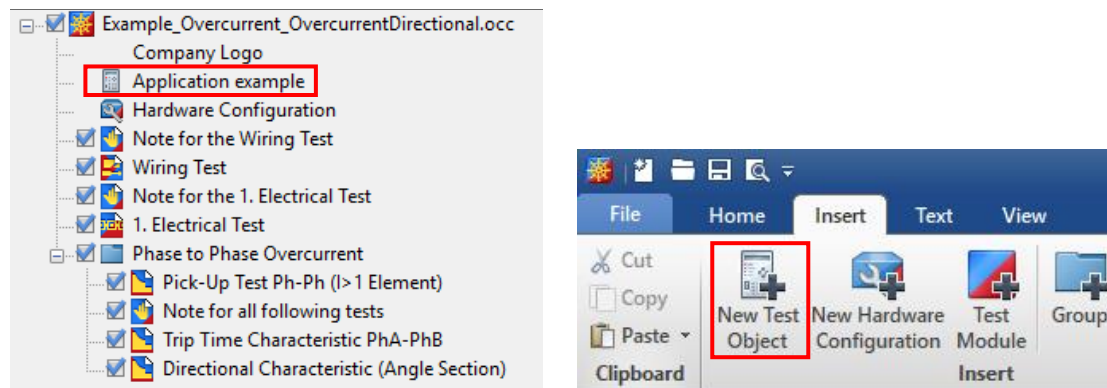
The *Overcurrent* test module is designed for testing directional and non-directional overcurrent protection functions with DTOC or IDMT tripping characteristics (short-circuit, thermal overload, zero sequence, negative sequence, and customized curve characteristics).

The test module can be found on the start screen of the OMICRON *Test Universe*. It can also be inserted into an OCC File (*Control Center* document).



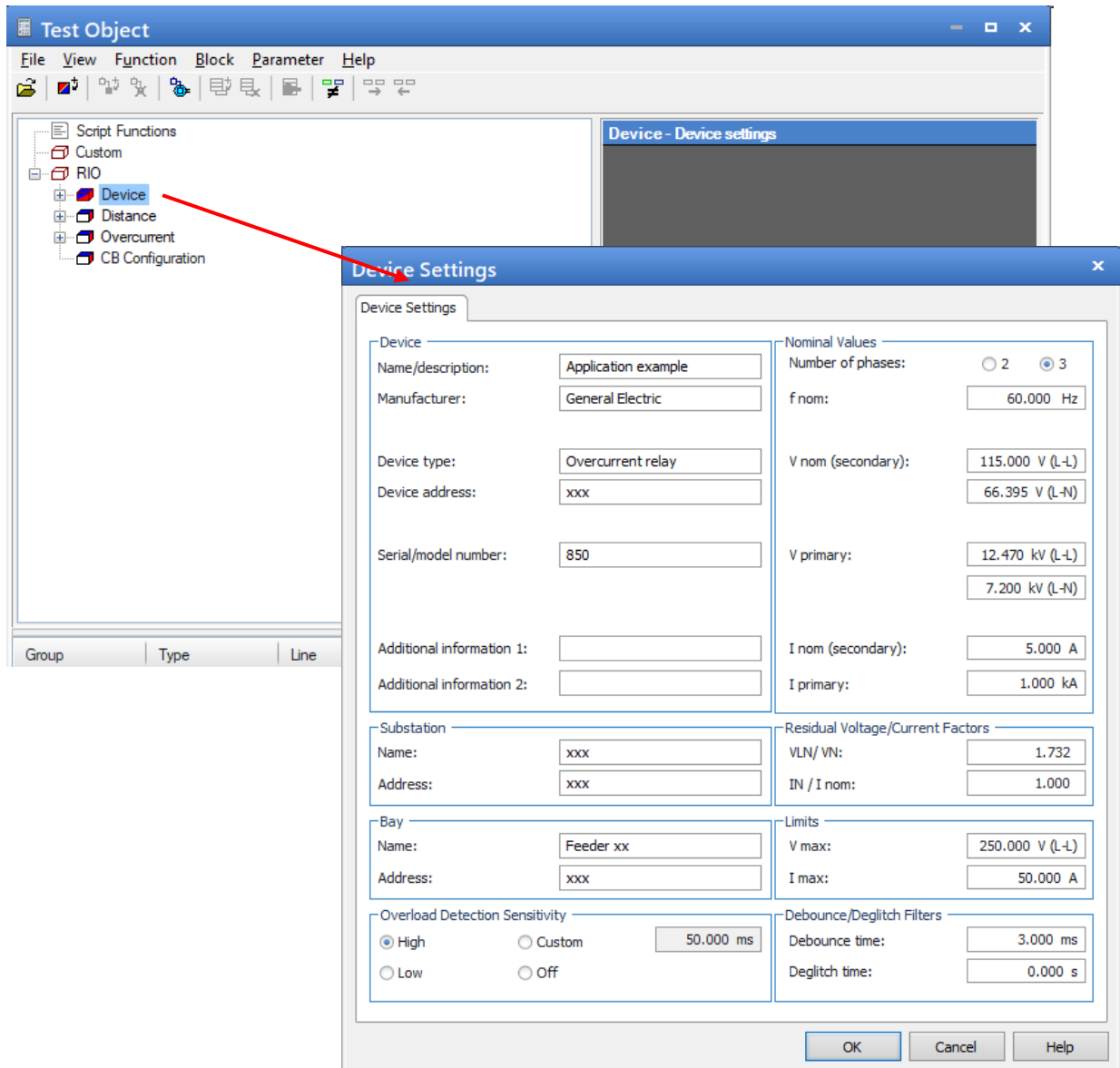
3.1 Defining the Test Object

Before you start your test, define the settings of the relay (the test object) to be tested. Double-click the **Test Object** entry in the Control Center file to launch **Test Object**. Alternatively, click **Test Object** on the **Insert** tab.



3.1.1 Device Settings

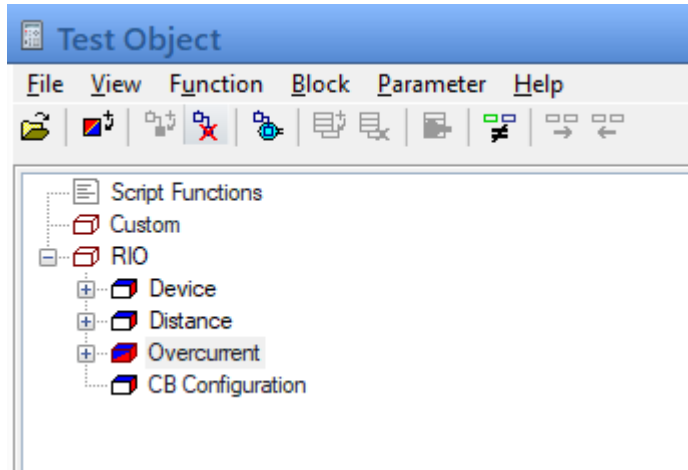
General relay settings (e.g., relay type, relay ID, substation details, CT and VT parameters) are entered in the **RIO** function **Device**.



Note: The parameters **V max** and **I max** limit the output of the currents and voltages to prevent damage to the device under test. These values must be adapted to the respective **Hardware Configuration** when connecting the outputs in parallel or when using an amplifier. The user should consult the manual of the device under test to make sure that its input rating will not be exceeded.

3.1.2 Defining the Overcurrent Protection Parameters

More specific data concerning the overcurrent relay can be entered in the **RIO** function **Overcurrent**. The definition of the overcurrent characteristic must also be made here.



Note: Once an *Overcurrent* test module is inserted this **RIO** function is available.

Relay Parameters

This first tab contains the definition of the directional behavior as well as the relay tolerances.

Overcurrent Protection Parameters

Relay Parameters Elements

Relay behavior

Directional behavior:

VT connection:

CT starpoint connection:

☐ Non-directional
☒ Directional

☒ At protected object
☐ Not at protected object

☒ To protected object
☐ From protected object

Tolerances

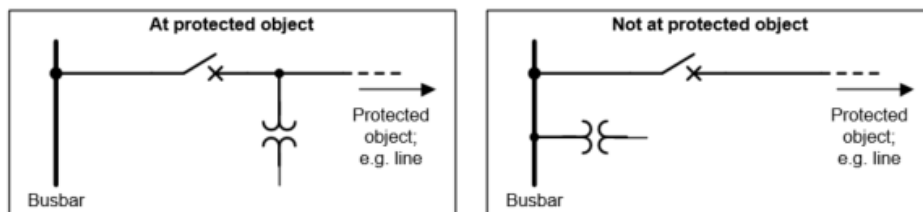
Current:

Time:

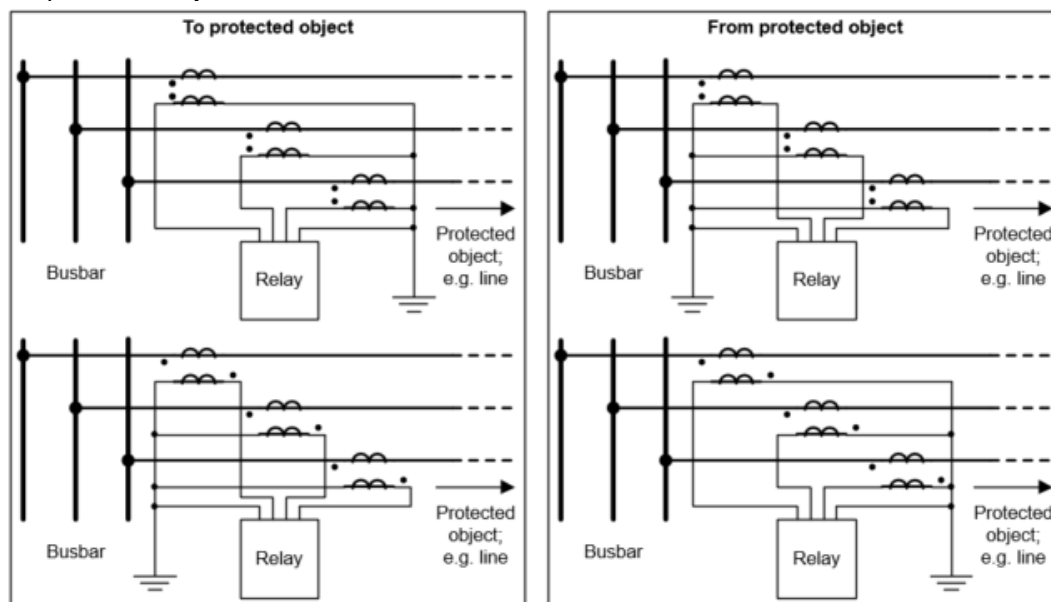
Relative:
 Absolute:

Relative:
 Absolute:

1. Since we want to test a directional overcurrent relay, this has to be activated.
2. Regarding the feeder connection diagram (Figure 1) the VT is placed **At protected object**. If **Not at protected object** is chosen, the voltage will have the nominal value after tripping.



3. The **CT starpoint connection** has to be set according to the connection of the secondary windings of the CT. For this example the feeder connection diagram (Figure 1) shows that the CT grounding is towards the protected object.



4. The current and time tolerances can be obtained from the relay manual.

Elements

This tab defines the characteristic of the different overcurrent elements.

Overcurrent Protection Parameters

Relay Parameters | **Elements**

Selected element type: **Phase (2 Elements / 2 Active)**

Active	Element Name	Tripping Characteristic	I Pick-up	Absolute	Time	Reset Ratio	Direction
<input checked="" type="checkbox"/>	TOC	IEEE VI	1.500 Iref	7.500 A	3.000	0.950	Forward
<input checked="" type="checkbox"/>	IOC	IEC Definite Time	3.000 Iref	15.00 A	100.0 ms	0.950	Non Directional

Define Element Characteristic | Define Element Directional Behavior | View Resulting Characteristic

Characteristic

Name: **IEEE VI**

$$t(s) = \frac{A \cdot Td + K1}{M^P - Q} + B \cdot Td + K2$$

M = Itest/Ipickup
Td = Time Index

A: 19.61 s B: 491.0 ms
P: 2.000 Q: 1.000
K1: 0.000 s K2: 0.000 s

I pick-up: 1.500 Iref Time index: 3.000

Range limits

☐ Active

I min: 0.000 Iref t min: 0.000 s

Manage / Select Characteristics

Characteristics tree:

- Favorites
 - Characteristics
 - Standard
 - IEC Definite Time**
 - IEC Normal Inverse
 - IEC Very Inverse
 - IEC Extremely Inverse
 - Predefined
 - Inverse
 - ANSI NI
 - ANSI VI
 - ANSI EI
 - ANSI LI
 - IEC / BS142 NI
 - IEC / BS142 VI
 - IEC / BS142 EI
 - IEC / BS142 LTI
 - IEC / BS142 STI
 - IEEE MI
 - IEEE VI
 - IEEE EI
 - IEEE Tab 1 MT - ABR

Characteristic definition: Name: **IEC Definite Time**

Range limits

☐ Active

I min: 0.000 Iref t min: 0.000 s
I max: +∞ Iref t max: +∞ s

Reset characteristic: ☒ Off

☐ Definite time tr:
☐ Inverse time R:
T:
T:

Select Close

The default overcurrent characteristic is shown above. It contains an IEC Definite Time scheme with one element for a phase overcurrent protection. This characteristic should be adjusted to the parameters of the relay (Table 1):

- In order to define the elements of the phase overcurrent protection, select **Phase** as the **Selected element type**.
Note: If other element types are also present in the relay select the related element types consecutively in (1) to enter these elements. The selection field shows the number of already defined related elements and how many of these are marked as active.
- This table shows the elements that define the tripping characteristic for the selected element type. The name of the first element may be changed according to the name used in the relay, e.g., "TOC".
- Change the characteristic type of the first element to **IEEE Very inverse** (Table 1).
- Afterwards set **I Pick-up** and the **Time index**.

- Now the second element can be added. It has an **IEC Definite Time** characteristic, which might be renamed to "IOC". Also set **I Pick-up** and the **Trip time**.

The list of the elements appearing after these adjustments is shown below.

Overcurrent Protection Parameters

Relay Parameters Elements

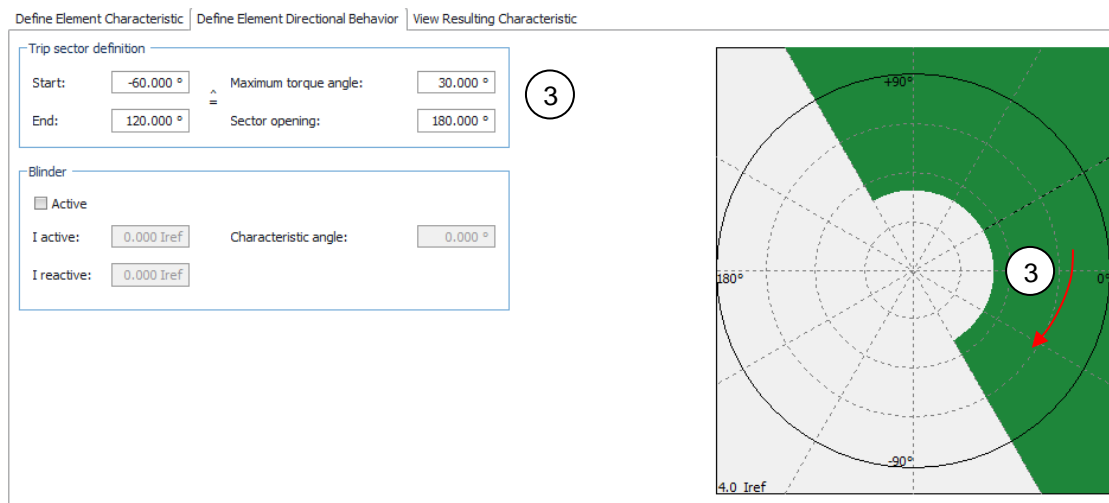
Selected element type: Phase (2 Elements / 2 Active)

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Add	Active	Element Name	Tripping Characteristic	I Pick-up	Absolute	Time	Reset Ratio	Direction
Copy To...	<input checked="" type="checkbox"/>	TOC	IEEE VI	1,500 Iref	7,500 A	3,000	0,950	Forward
Remove	<input checked="" type="checkbox"/>	IOC	IEC Definite Time	3,000 Iref	15,00 A	100,0 ms	0,950	Non Directional

- The **Reset Ratio** must also be checked in the manual.
- In order to define the directional behavior, the **Direction** of the 1st element has to be set to **Forward**.
Note: This setting is an orientation help for the reader and, once it is set, it will rotate the directional limits by 180° if changed to **Backward**.

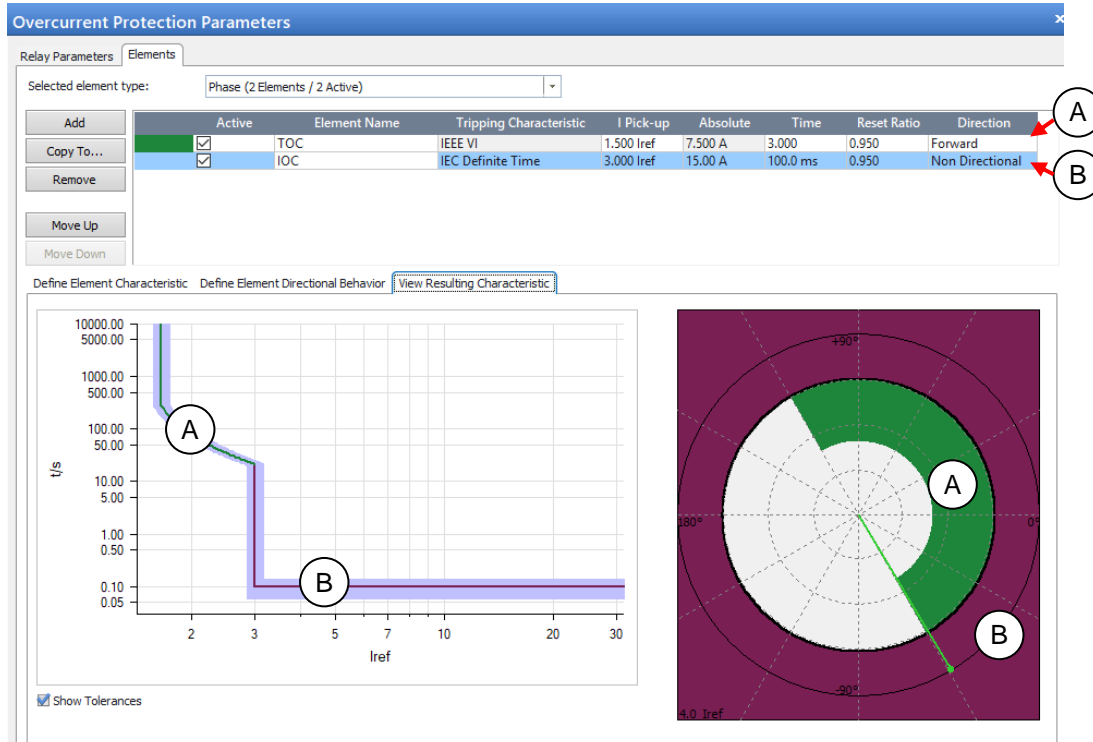
The adjustments of the directional characteristic must be made in the tab **Define Element Directional Behavior**:



As the relay characteristic angle cannot be entered in the **Test Object** directly, the **Trip sector definition** has to be calculated

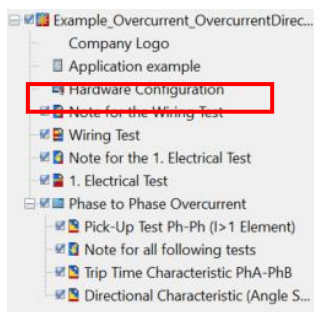
The resulting overcurrent characteristic is shown below.

- A 1st element
- B 2nd element

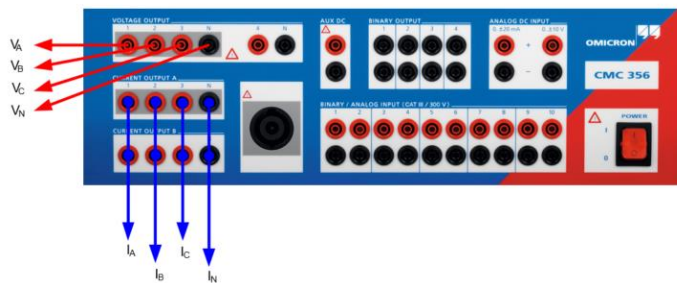
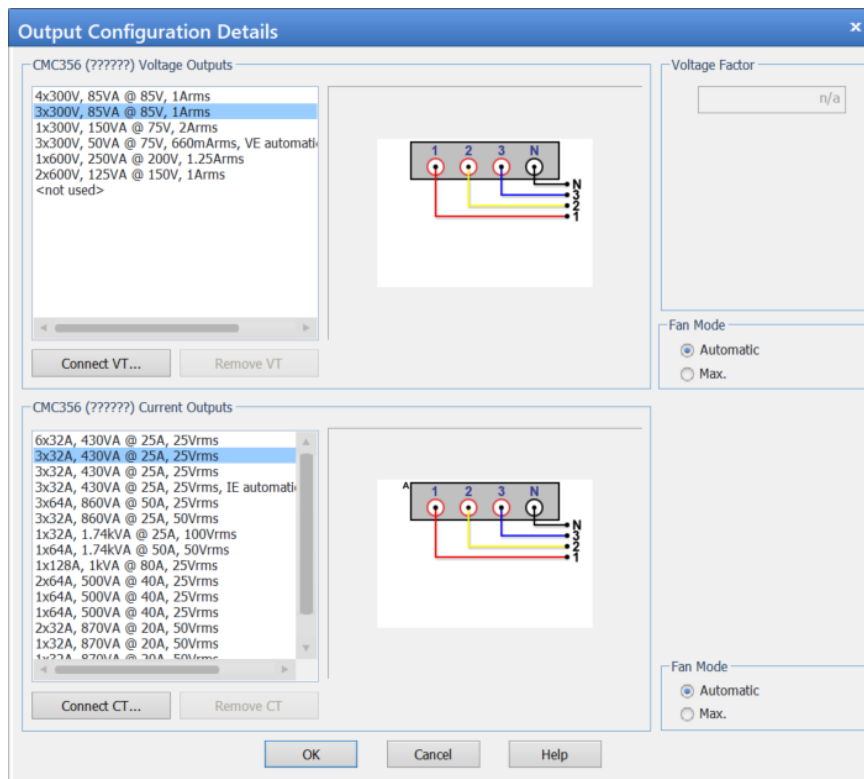


3.2 Global Hardware Configuration of the CMC Test Set

The global **Hardware Configuration** specifies the general input/output configuration of the CMC test set. It is valid for all subsequent test modules and, therefore, it has to be defined according to the relay's connections. It can be opened by double-clicking the **Hardware Configuration** entry in the OCC file.



3.2.1 Example Output Configuration for Protection Relays with a Secondary Nominal Current of 5 A



3.2.2 Analog Outputs

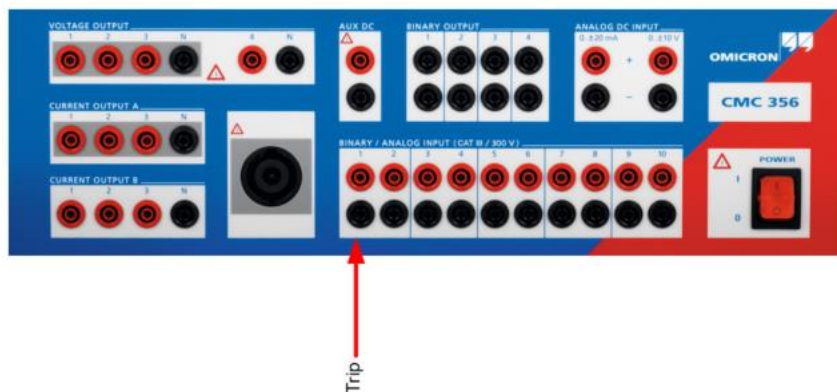
Global Hardware Configuration									
General Analog Outputs Binary / Analog Inputs Binary Outputs DC Analog Inputs Time Source									
		CMC356 V A ??????				CMC356 I A ??????			
Display Name	Connection Terminal	1	2	3	N	1	2	3	N
V A-N		X							
V B-N			X						
V C-N				X					
I A						X			
I B							X		
I C								X	

The analog outputs, binary inputs and outputs can all be activated individually in the local **Hardware Configuration** of the specific test module (see chapter 3.3 “Local Hardware Configuration for Directional Overcurrent Testing”).

3.2.3 Binary Inputs

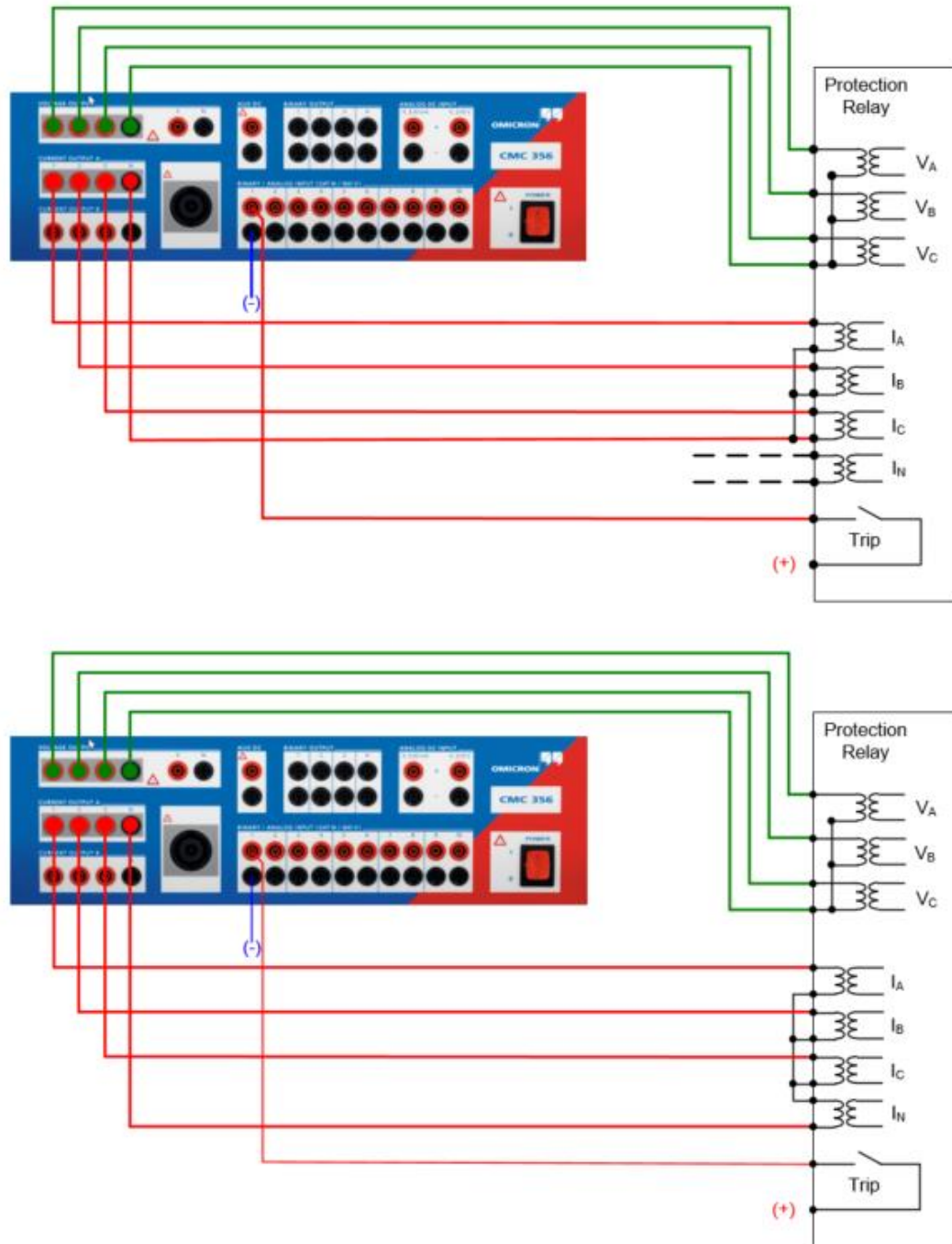
Global Hardware Configuration									
General Analog Outputs Binary / Analog Inputs Binary Outputs DC Analog Inputs Time Source									
		Function							
		Binary		Binary		Binary		Binary	
		<input type="checkbox"/>		<input type="checkbox"/>		<input checked="" type="checkbox"/>		<input checked="" type="checkbox"/>	
		120 V		120 V					
		84 V		84 V					
Display Name	Connection Terminal	1+	1-	2+	2-	3+	3-	4+	4-
Trip		X							
Bin. in 2				X					
Bin. In. 3						X			
Bin. In. 4								X	

1. The trip command has to be connected to a binary input. BI1 ... BI10 can be used.
2. For wet contacts adapt the nominal voltages of the binary inputs to the voltage of the circuit breaker trip command or select **Potential Free** for dry contacts.
3. The binary outputs and analog DC inputs will not be used for the following tests.



3.2.4 Wiring of the Test Set for Relays with a Secondary Nominal Current of 5A

Note: The following wiring diagrams are examples only. The wiring of the analog current inputs may be different if additional protective functions such as sensitive ground fault protection are provided. In this case I_N may be wired separately.



3.3 Local Hardware Configuration for Directional Overcurrent Testing



The local Hardware Configuration activates the outputs/inputs of the CMC test set for the selected test module. Therefore, it has to be defined for each test module separately. It can be opened by clicking the **Hardware Configuration** button in the test module.

3.3.1 Analog Outputs

Local Hardware Configuration

General Analog Outputs Binary / Analog Inputs Binary Outputs DC Analog Inputs

The read-only settings on this page can be edited in the Global Hardware Configuration, only. More...

Test Module Output Signal	Display Name	Connection Terminal	CMC356 V A				CMC356 I A				
			1	2	3	N	1	2	3	N	
VA-N	VA-N		X								
VB-N	VB-N			X							
VC-N	VC-N				X						
IA	IA						X				
IB	IB							X			
IC	IC								X		

3.3.2 Binary Inputs

Local Hardware Configuration

General Analog Outputs Binary / Analog Inputs Binary Outputs DC Analog Inputs

The read-only settings on this page can be edited in the Global Hardware Configuration, only. More...

Test Module Input Signal	Display Name	Connection Terminal	CMC356															
			1+	1-	2+	2-	3+	3-	4+	4-	5+	5-	6+	6-	7+	7-	8+	8-
Trip	Trip		X															
Not used	Bin. In. 2				X													
Not used	Bin. In. 3					X												
Not used	Bin. In. 4						X											
Not used	Bin. In. 5							X										

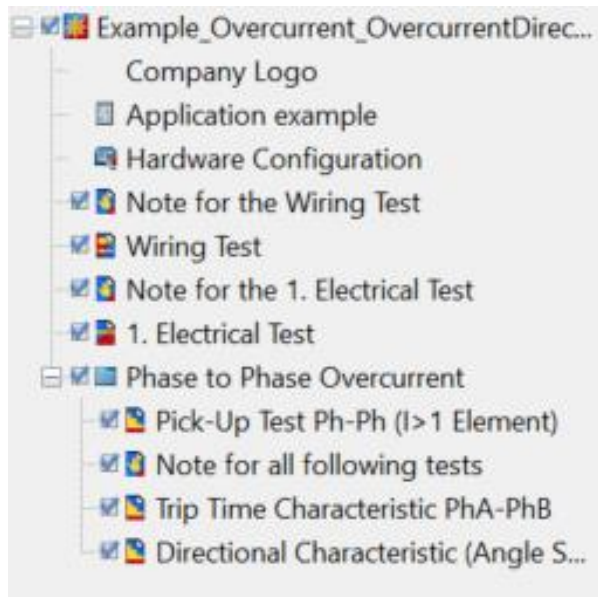
3.4 Defining the Test Configuration

3.4.1 General Approach

When testing the directional overcurrent protection, the following steps are recommended:

- > **Trip time characteristic:** Verifying the trip times of every element of the tripping characteristic.
- > **Directional characteristic:** Verifying the angle of the directional characteristic.

Each of these tests can be performed with the *Overcurrent* test module.



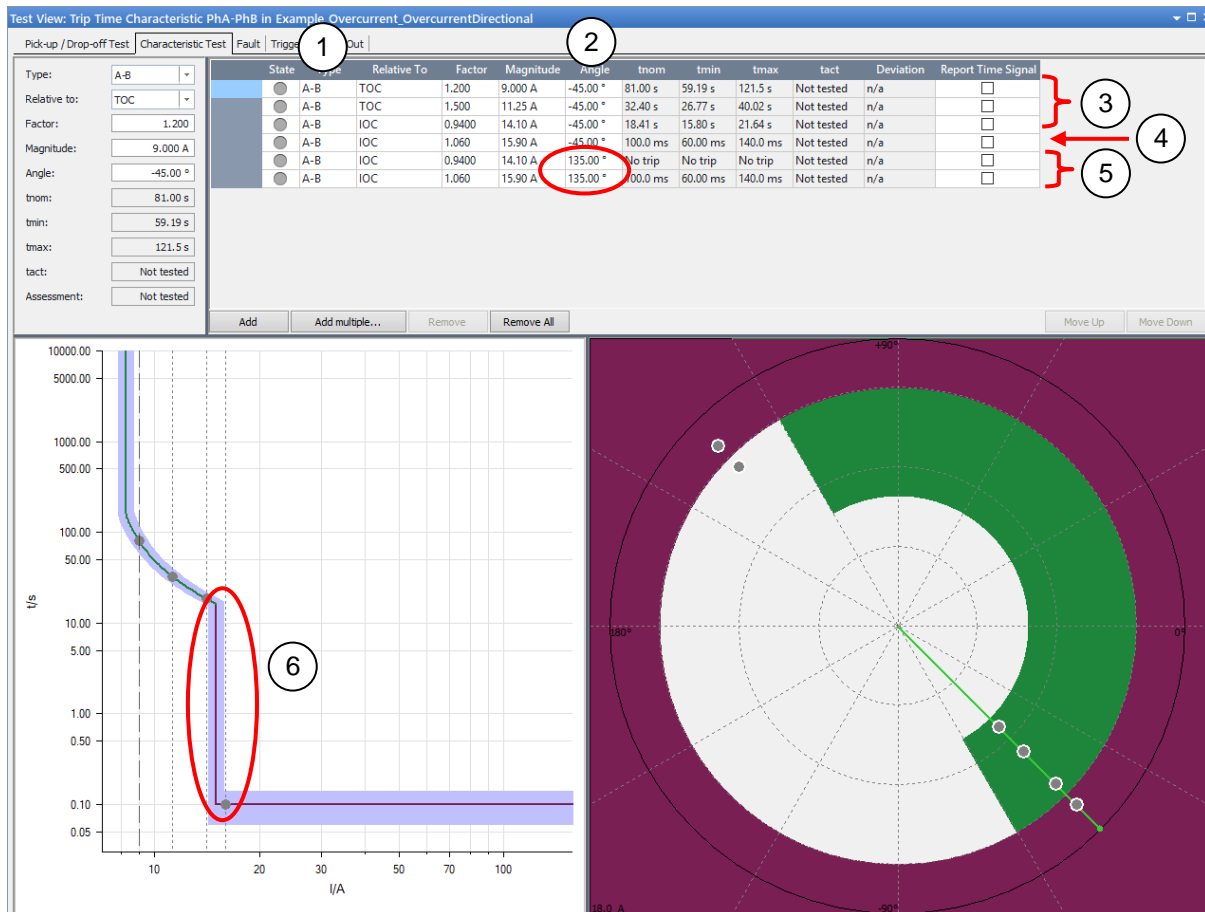
3.4.2 Trip Time Characteristic Test

Trigger and Fault tabs:

The image displays two side-by-side screenshots of a software interface for configuring a 'Trip Time Characteristic' test. The left screenshot shows the 'Trigger' tab, and the right screenshot shows the 'Fault' tab. Both tabs are part of a larger window titled 'Test View: Trip Time Characteristic PhA-PhB in Example_Overcurr...'. The 'Trigger' tab includes a 'Start condition' section with 'Time reference' set to 'Fault inception' and a 'Trip condition' section with 'Trigger logic' set to 'AND'. A table with one row shows 'Input Display Name State' with values '1 Trip 1'. The 'Fault' tab includes 'Load settings' with 'Load current' set to '0.000 A', 'Load angle (I)' set to '0.00 °', and 'Decaying DC' set to 'Active'. It also has 'Time settings' with 'Prefault time' set to '100.0 ms', 'Postfault time' set to '500.0 ms', 'Absolute max. time' set to '140.0 s', and 'Relative max. time' set to '100 %'. The 'Voltage settings' section includes 'Enable voltage output' checked, 'Fault voltage LN' set to '30.00 V', 'Nominal voltage LN' set to '66.395 V', 'Fault voltage LL' set to '51.96 V', and 'Nominal voltage LL' set to '115.000 V'. The 'Thermal image' section has 'Enable reset' checked and 'Reset method' set to 'Manual'. The 'CB characteristic' section has 'Min. time' set to '50.00 ms'. The 'Edit Message...' button is visible at the bottom right of the 'Fault' tab.

1. The trigger for this test will be the trip contact.
2. A **Load current** during the pre-fault state will not be used in this example.
3. The **Absolute max. time** has to be adjusted. On the one hand, it has to exceed the upper tolerance of the test point with the longest trip time otherwise an assessment will not be possible. On the other hand, it should not be set to an unnecessarily high value. For shots where **No trip** is expected this will be the waiting time until the assessment 'no trip' is made before continuing with the next shot. So if this time is set to a very high value, it would unnecessarily prolong the test duration.
4. The **Voltage settings** define the voltages for different fault types. These voltages only apply to the faulty phases. The non-faulty phases remain at nominal voltage. For most purposes it is sufficient to leave these settings at the default values.

Characteristic Test tab:



1. As the function to test is a phase overcurrent function, a phase to phase fault is used.

Note: In this case other protection functions may interfere with the test. However, if such functions or elements (e.g., ground fault protection, negative sequence protection, etc.) are present they may be specified in the **Test Object** in the same manner as the phase elements were entered in this example. The resulting characteristic will be calculated individually and shown for each test shot depending on its fault type (1) and fault angle (2), ensuring a proper assessment according to the expected overall relay behavior.

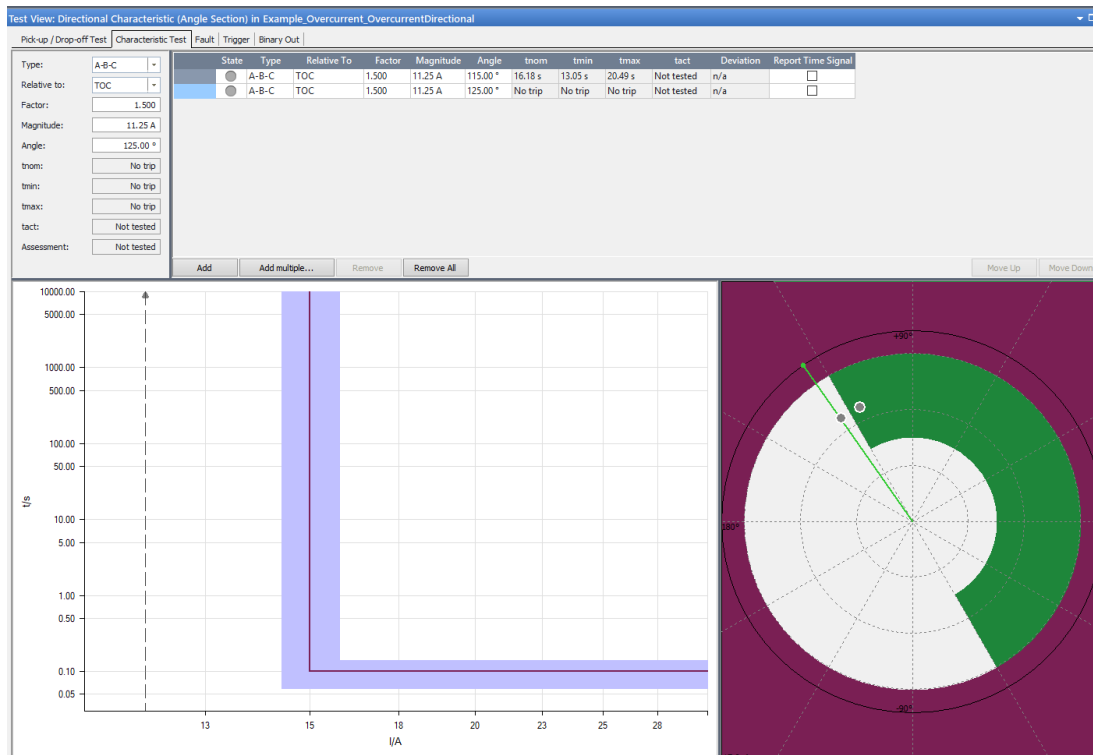
2. The **Angle** for the forward direction should be the **Maximum torque angle**. For reverse direction it has to be entered shifted by 180°.
3. As the trip time of the TOC element depends on the current, this element has to be verified with more than one test point.
4. The trip time of the 2nd element can be confirmed with only one test point.
5. The directional behavior is confirmed with one shot at each stage in reverse direction.
6. The value of the 2nd element is also confirmed by placing two test points outside of the tolerance band of this setting.

Instead of directly entering the magnitude value it can be expressed by its relation to an element setting, e.g., set **Relative to:** to the 2nd element and set the **Factor** to 1.06 (i.e., 6% above the threshold) or 0.94 (i.e., 6% below the threshold).

Note: Regarding the ways to enter and modify test data please also consult the Help section of the module (press F1).

3.4.3 Directional Characteristic Test

The **Trigger** and **Load settings** are the same as explained for the trip time characteristic test. The **Absolute max. time** can be reduced because the test current will be set shortly below the lower tolerance of the 2nd element pick-up value.



As this test confirms the angle of the directional characteristic, the test points should be placed on both sides of the directional characteristic line. In order to get a correct assessment they should be placed just outside of the angle tolerance.

Note: A three phase fault is recommended for this test. The angle between current and voltage for each phase is the same for this fault type. This ensures a proper assessment of the test.

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